Refine Search

Search Results -

Term	Documents
(9 AND 26).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	39
(L26 AND L9).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	39

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
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Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L32

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Search History

DATE: Wednesday, May 30, 2007 **Purge Queries** Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L32	L26 and l9	39	L32
L31	L26 and l8	0	L31
L30	L26 and l7	5	L30
L29	L26 and l6	5	L29
L28	L26 and l5	15	L28
L27	L26 and l4	46	L27
L26	l16 and instruction$1 near6 decod$4	198	L26
DB=PGPB,USPT; PLUR=YES; OP=OR			
L25	L24 and l16	110	L25
L24	(710/22,23)[CCLS]	1454	L24
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			

L23	L17 and l9	128	L23
L22	L17 and l8	2	L22
L21	L17 and l7	50	L21
L20	L17 and l6	50	L20
L19	L17 and l5	15	L19
L18	L17 and l4	54	L18
L17	L16 and (integrated or chip or semiconductor or monolithic)	808	L17
L16	(gp or general near1 purpose near1 register$1) near55 (dma or direct near1 memory near1 access$3) and (reconfigur$6 or configur$6 or extension$3 or extend$5)	1355	L16
	DB=PGPB,USPT; PLUR=YES; OP=OR		
L15	l3 and l9	126	L15
L14	l3 and l8	2	L14
L13	l3 and l7	220	L13
L12	l3 and l6	220	L12
L11	l3 and l5	254	L11
L10	l3 and l4	516	L10
L9	(710/22-74)[CCLS]	11861	L9
L8	(711/112)[CCLS]	1963	L8
L7	(711/112-173)[CCLS]	25652	L7
L6	(711/112-173)![CCLS]	25652	L6
L5	(712/208-229)[CCLS]	6489	L5
L4	(712/2-300)[CCLS]	13389	L4
	DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
L3	L2 and instruction$1 near6 decod$4	1882	L3
L2	L1 and (integrated or chip or semiconductor or monolithic)	14691	L2
L1	(gp or general near1 purpose near1 register$1) and (dma or direct near1 memory near1 access$3) and (reconfigur$6 or configur$6 or extension$3 or extend$5)	22343	L1

END OF SEARCH HISTORY

Results for "((dma <and> instruction* <and> decod*)<in>metadata)" e-mail

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((dma <and> instruction* <and> decod*)<in>metadata) Search

Check to search only within this results set

» Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

Display Format: Citation Citation & Abstract

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1. **A microprocessor with a 128-bit CPU, ten floating-point MAC's, four floating-point dividers, : decoder**
Suzuoki, M.; Kutaragi, K.; Hiroi, T.; Magoshi, H.; Okamoto, S.; Oka, M.; Ohba, A.; Yamamoto, Y.; F M.; Yutaka, T.; Okada, T.; Nagamatsu, M.; Urakawa, Y.; Funyu, M.; Kunimatsu, A.; Goto, H.; Hashi Murakami, H.; Ohtaguro, Y.; Aono, A.;
Solid-State Circuits, IEEE Journal of
Volume 34, Issue 11, Nov. 1999 Page(s):1608 - 1618
Digital Object Identifier 10.1109/4.799870
AbstractPlus | References | Full Text: PDF(640 KB) IEEE JNL
Rights and Permissions

2. **A236 parallel DSP chip provides real-time video processing economically and efficiently**
Morton, S.G.;
ELECTRO '96. Professional Program. Proceedings.
30 April-2 May 1996 Page(s):261 - 268
Digital Object Identifier 10.1109/ELECTR.1996.501237
AbstractPlus | Full Text: PDF(628 KB) IEEE CNF
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3. **A 100 mm^2 0.95 W single-chip MPEG2 MP@ML video encoder with a 128GOPS motion estim tasking RISC-type controller**
Miyagoshi, E.; Araki, T.; Sayama, T.; Ohtani, A.; Minemaru, T.; Okamoto, K.; Kodama, H.; Morishig Aoki, K.; Mitsumori, T.; Imanishi, H.; Jinbo, T.; Tanaka, Y.; Taniyama, M.; Shingou, T.; Fukumoto, T K.;
Solid-State Circuits Conference, 1998. Digest of Technical Papers. 45th ISSCC 1998 IEEE Interna
5-7 Feb. 1998 Page(s):30 - 31, 409
Digital Object Identifier 10.1109/ISSCC.1998.672363
AbstractPlus | Full Text: PDF(796 KB) IEEE CNF
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4. **An ASIC RISC-based I/O processor for computer applications**
Cates, R.L.; Farrell, J.J., III;
Euro ASIC '90
29 May-1 June 1990 Page(s):50 - 55
Digital Object Identifier 10.1109/EASIC.1990.207909
AbstractPlus | Full Text: PDF(484 KB) IEEE CNF
Rights and Permissions

5. **CPU for PlayStation(R)2**
Tago, H.; Hashimoto, K.; Ikumi, N.; Nagamatsu, M.; Suzuoki, M.; Yamamoto, Y.;
Design, Automation and Test in Europe, 2001. Conference and Exhibition 2001. Proceedings
13-16 March 2001 Page(s):696
Digital Object Identifier 10.1109/DATE.2001.915101
AbstractPlus | Full Text: PDF(40 KB) IEEE CNF
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